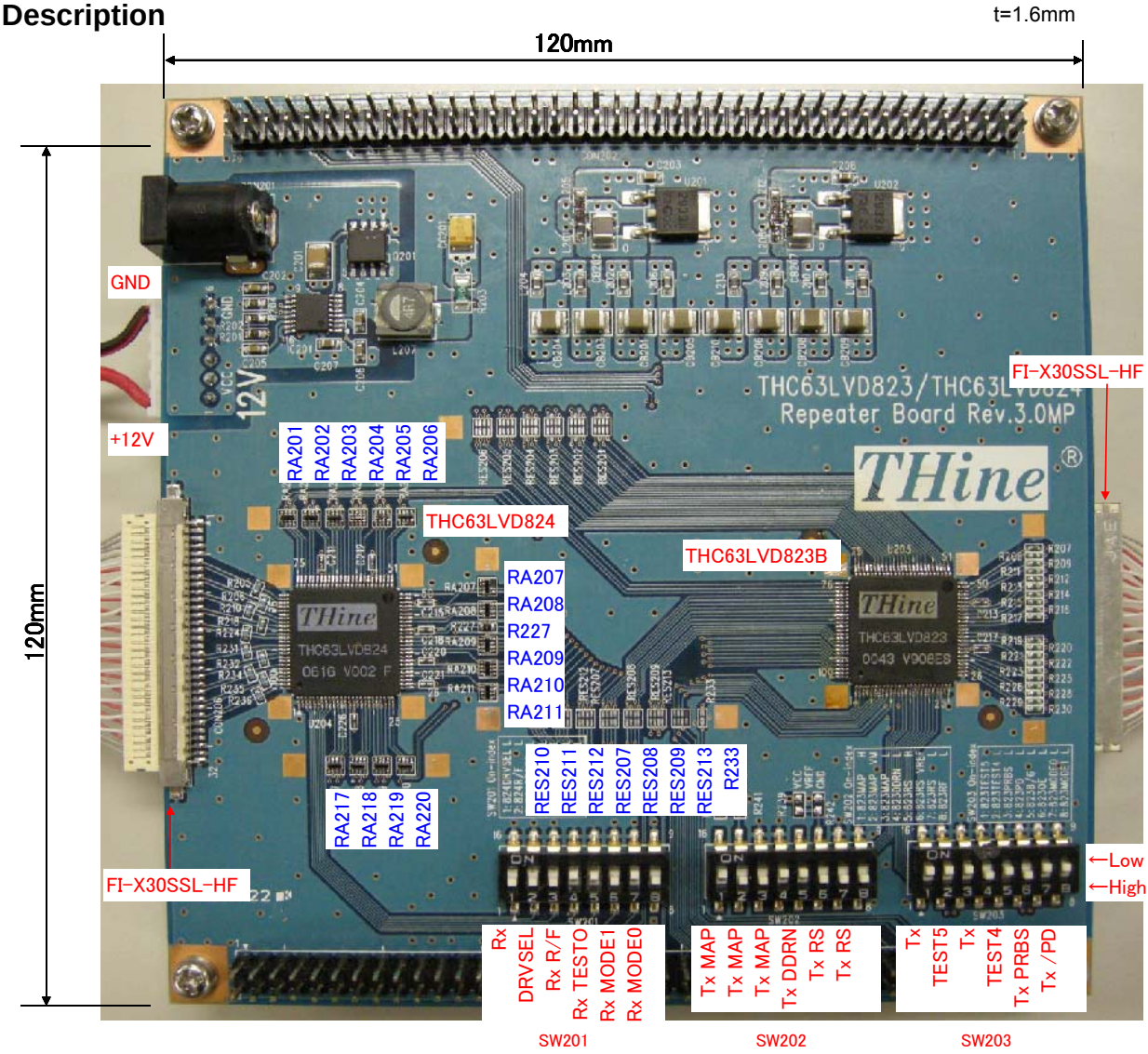
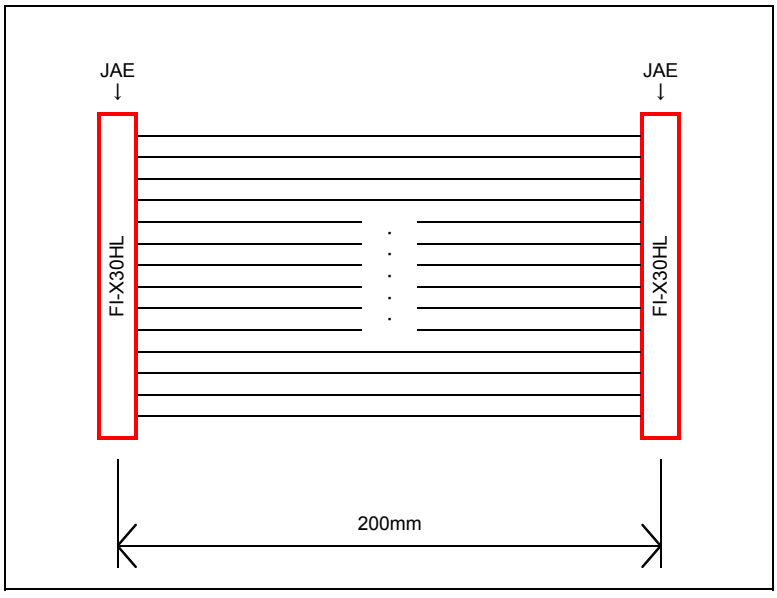
Description



LVDS-Cable Type.



SW201 Setting

* Def. : Default Setting

SW Pin#	* Def.	NodeName	THC63LVD824		
			IC Pin#	PinName	Description
1	H	RX DRVSEL	9	DRVSEL	Output Driverbility Select. H : High power, L : Low power.
2	H	RX R/F	8	R/F	Output Clock Triggering Edge Select. H : Rising edge, L : Falling edge.
3	L	RX TESTO	7	GND	Ground Pins for TTL outputs and digital circuitry.
4	L	RX MODE1	6	MODE1	Pixel Data Mode.
5	L	RX MODE0	5	MODE0	
6	H	RX /PD	4	/PDWN	H : Normal operation, L : Power down (all outputs are pulled to ground)
7	L	RX TEST1	3	GND	Ground Pins for TTL outputs and digital circuitry.
8	H	N/C	-	-	Non Connected.

MODE1	MODE0	Mode
L	L	Dual Link
L	H	Single Link

SW202 Setting

* Def. : Default Setting

SW Pin#	* Def.	Node Name	THC63LVD823B		
			IC Pin#	PinName	Description
1	L	TX MAP	14	MAP	LVDS mapping table select.
2	H				
3	H				
4	L	TX DDRN	13	DDRN	DDR function is active when MODE<1:0> = HL(Single-in/Dual-out mode). Open or H : DDR(Double Edge input) function disable. L : DDR(Double Edge input) function enable.
5	L	TX RS	12	RS	LVDS swing mode, VREF select.
6	H				
7	H				
8	H	TX RF	11	R/F	Input Clock Triggering Edge Select. H : Rising edge, L : Falling edge

SW-Pin#			RS	Mapping Mode
1	2	3		
L	H(open)	H(open)	VIHM	Mapping MODE1
H(open)	L	H(open)	VIMM	Mapping MODE2
H(open)	H(open)	L	VILM	Reserved

SW-Pin#			RS	LVDS Swing	Small Swing Input Support
5	6	7			
L	H(open)	H(open)	VIHM	350mV	N/A
H(open)	L	H(open)	VIMM	350mV	RS=VREFa
H(open)	H(open)	L	VILM	200mV	N/A

a. VREF is Input Reference Voltage.

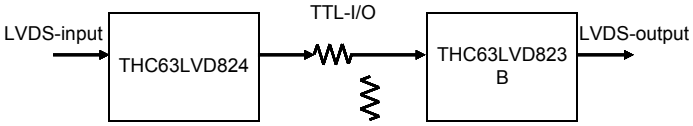
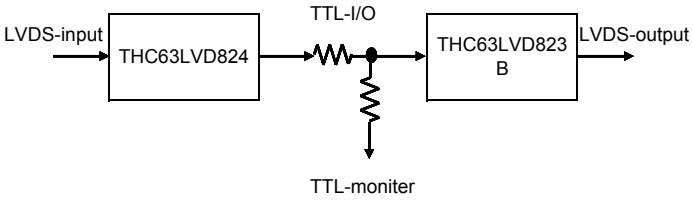
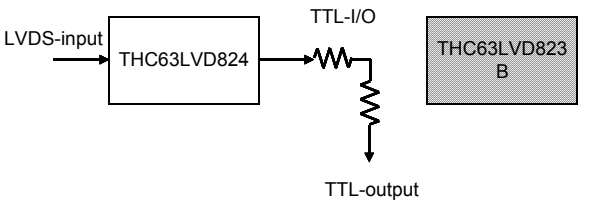
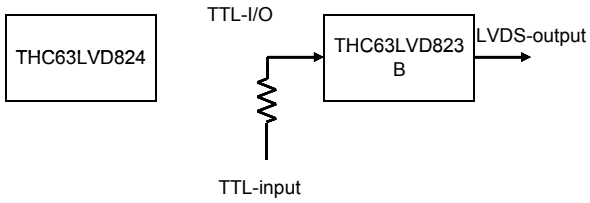
SW203 Setting

* Def. : Default Setting

SW Pin#	* Def.	NodeName	THC63LVD823B		
			IC Pin#	PinName	Description
1	H	TX TEST5	22	N/C	Must be Open.
2	L	TX TEST4	21	Reserved	Must be tied to GND.
3	L	TX PRBS	20	PRBS	PRBS(Pseudo-Random Binary Sequence) generator is active in order to evaluate eye patterns when MODE<1:0> = LL(Dual-in/Dual-out mode). H : PRBS generator is enable. L : Normal Operation
4	H	TX /PD	19	/PDWN	H : Normal operation, L : Power down (all outputs are Hi-Z)
5	L	TX 8/6	18	GND	Ground Pins for TTL inputs and digital circuitry.
6	H	TX OE	17	OE	Output enable. H : Output enable, L : Output disable (all outputs are Hi-Z).
7	L	TX MODE0	16	MODE0	Pixel Data Mode
8	L	TX MODE1	15	MODE1	

MODE1	MODE0	Mode
L	L	Dual Link(Dual-in/Dual-out)
H	L	Dual Link(Single-in/Dual-out)
L	H	Single Link(Dual-in/Single-out)
H	H	Single Link(Single-in/Single-out)

Measures Type

#	Type	Un-Mount	0Ω-Mount	33Ω-Mount
1		RES207 RES208 RES209 RES210 RES211 RES212 RES213 R233		RA201 RA211 RA202 RA217 RA203 RA218 RA204 RA219 RA205 RA220 RA206 R227 RA207 RA208 RA209 RA210
2			RES207 RES208 RES209 RES210 RES211 RES212 RES213 R233	RA201 RA211 RA202 RA217 RA203 RA218 RA204 RA219 RA205 RA220 RA206 R227 RA207 RA208 RA209 RA210
3		THC63LVD823B	RES207 RES208 RES209 RES210 RES211 RES212 RES213 R233	RA201 RA211 RA202 RA217 RA203 RA218 RA204 RA219 RA205 RA220 RA206 R227 RA207 RA208 RA209 RA210
4		RA201 RA211 RA202 RA217 RA203 RA218 RA204 RA219 RA205 RA220 RA206 R227 RA207 RA208 RA209 RA210	RES207 RES208 RES209 RES210 RES211 RES212 RES213 R233	

Notices and Requests

1. The product specifications described in this material are subject to change without prior notice.
2. The circuit diagrams described in this material are examples of the application which may not always apply to the customer's design. We are not responsible for possible errors and omissions in this material. Please note if errors or omissions should be found in this material, we may not be able to correct them
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4. Note that if infringement of any third party's industrial ownership should occur by using this product, we will be exempted from the responsibility unless it directly relates to the production process or functions of the product.
5. This product is presumed to be used for general electric equipment, not for the applications which require very hi reliability (including medical equipment directly concerning people's life, aerospace equipment, or nuclear contro equipment). Also, when using this product for the equipment concerned with the control and safety of the transportation means, the traffic signal equipment, or various Types of safety equipment, please do it after applyi appropriate measures to the product.
6. Despite our utmost efforts to improve the quality and reliability of the product, faults will occur with a certain small probability, which is inevitable to a semi-conductor product. Therefore, you are encouraged to have suffici redundant or error preventive design applied to the use of the product so as not to have our product cause any soc or public damage.
7. Please note that this product is not designed to be radiation-proof.
8. Customers are asked, if required, to judge by themselves if this product falls under the category of strategic good under the Foreign Exchange and Foreign Trade Control Law.

THine Electronics, Inc.

sales@thine.co.jp