

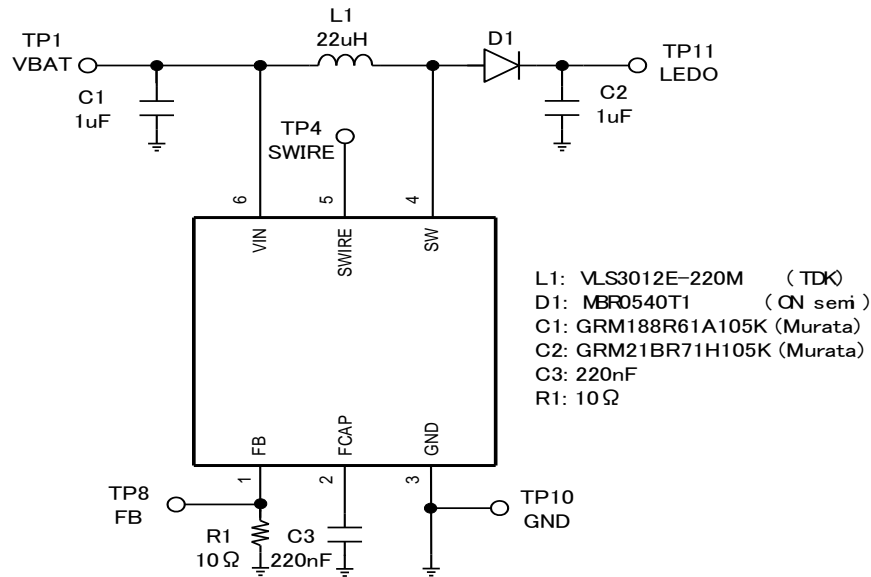


Test Procedure for the LV52204MUGEVB Evaluation Board

1. Evaluation Board Spec

Evaluation Board	2.7~5.5V
FB voltage	0~200mV
Over Voltage Protection	38V
Oscillation Frequency	600kHz

2. Evaluation Board Schematic



3. Pin Function

PIN #	Pin Name	Description
1	FB	Feedback pin.
2	FCAP	Filtering capacitor terminal for PWM mode.
3	GND	Ground
4	SW	Switch pin. Drain of the internal power FET.
5	SWIRE	1-wire dimming control and PWM dimming input(active High).
6	VCC	Supply voltage.
	Expose-pad	Connect to GND on PCB.



4. Start/Shutdown sequences

1. Please input "High" into SWIRE PIN during the period that is longer than $T_{on}(20\mu Sec)$ to start IC.
2. Then, please select a mode during mode select period(T_{sel}) .When you select Digital Mode, please input "Low" longer than T_{w1} after "High" longer than $T_{w0}(100\mu Sec)$ within $T_{sel}(1mSec)$ period. It becomes PWM mode if you fail to set Digital mode in specified timing period.

In the case of PWM frequency is less than 6.6kHz, it may become Digital Mode when you input a narrow pulse of Duty.To evade it, input "High" that is longer than $T_{sel}(2.2mSec)$, and, please input PWM pulse afterwards.

3. IC shut down when you make SWIRE PIN Low longer than $T_{off}(8.9ms)$ period.

The Data register is stored at this point. The reset of the power supply is necessary to clear it. In addition, the mode is initialized when you shut down IC.Please make mode select each time you reboot.

