

Super Low Distortion Multilayer Ceramic Capacitors

TMK316SD104KL-T



■ Features

- Item Summary
0.1 μ F \pm 10%, 25V,
Standard, 1206/3216 (EIA/JIS)
- Lifecycle Stage
Mass Production
- Standard packaging quantity (minimum)
Taping Embossed 2000pcs

■ Products characteristics table

Capacitance	0.1 μ F \pm 10 %
Case Size (EIA/JIS)	1206/3216
Rated Voltage	25 V
$\tan \delta$ (max)	0.1 %
Operating Temp. Range (EIA)	-55 to +125 °C
Operating Temp. Range (JIS)	-55 to +125 °C
High Temperature Loading (% Rated Voltage)	200 %
Insulation Resistance (min)	500 M Ω · μ F
RoHS2 Compliance (10 subst.)	Yes
REACH Compliance (173 subst.)	Yes
Halogen Free	Yes
Soldering	Reflow

■ External Dimensions

Dimension L	3.2 \pm 0.15 mm
Dimension W	1.6 \pm 0.15 mm
Dimension T	1.6 \pm 0.20 mm
Dimension e	0.50 +0.35/-0.25 mm

2017.04.30

The data is reference only. Electrical characteristics vary depending on environment or measurement condition.
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 Before making final selection, please check product specification.

Multi Layer Ceramic Capacitor **TAIYO YUDEN**

-Electrical Characteristics Data-

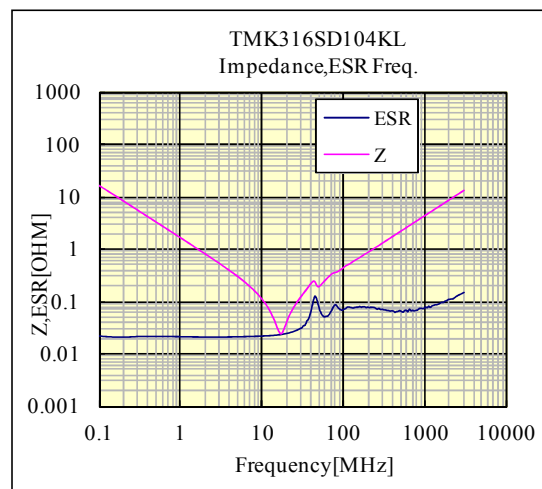
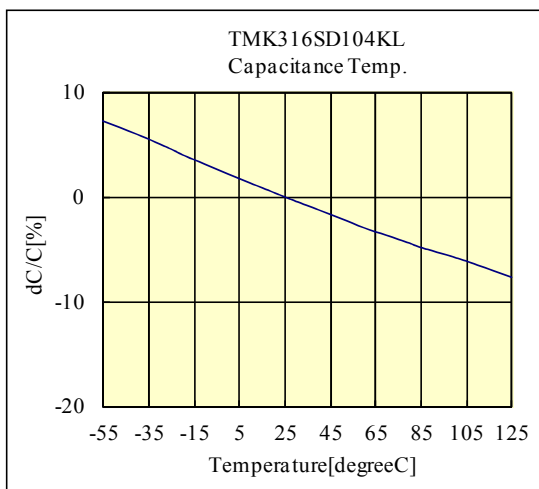
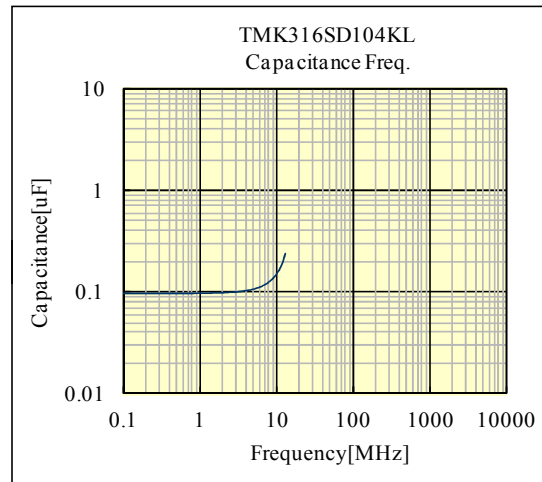
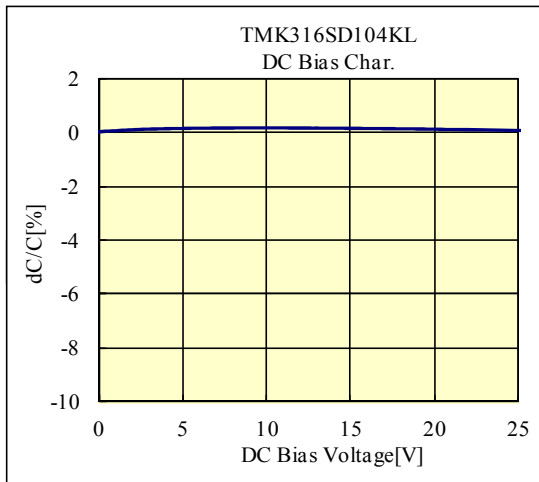
16CJA02

RoHS compliance product

○ **TMK316SD104KL**

25V 3216 0.1uF +/- 10% t=1.8mmMAX

25V 1206 0.1uF +/- 10% t=0.071inchMAX



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