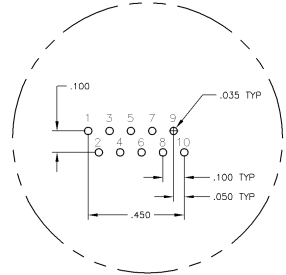
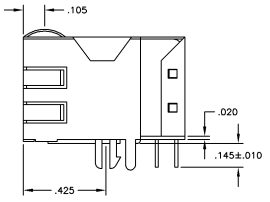
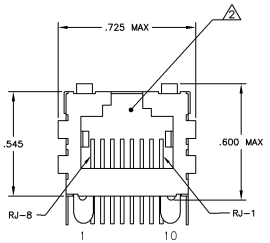
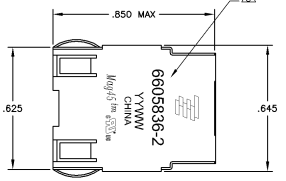
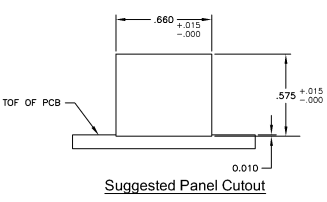


MECHANICAL:

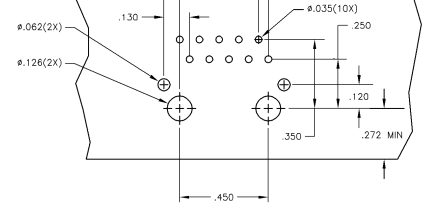
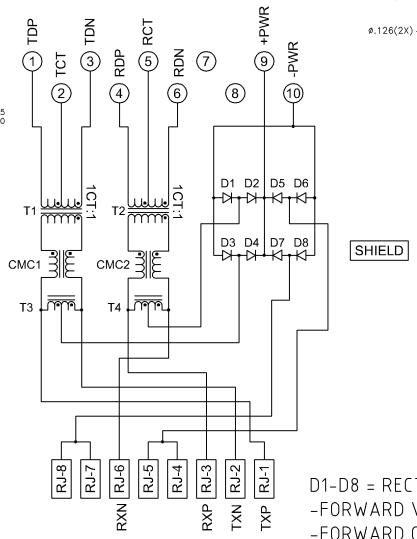


Pin Designations



Suggested Panel Cutout

7D15P1 SERIES MAGNETIC CIRCUIT



Suggested PCB Layout (Component Side)

D1-D8 = RECTIFIER DIODE:
 -FORWARD VOLTAGE, VF = 1.2V MAX @ IF=0.5A
 -FORWARD CURRENT, IF = 0.5A MAX
 -REVERSE VOLTAGE, VR = 80V MAX

- △ MATERIALS
 - HOUSING - THERMOPLASTIC PET POLYESTER FLAMMABILITY RATINGS UL 94V-0
 - SHIELD - .010" THICK C26800 BRASS PREPLATED WITH 30µINCH MIN SEMI-BRIGHT NICKEL, SOLDER TABS POST DIPPED WITH 100µINCH MIN SAC SOLDER
 - MOD JACK CONTACTS - 0.0157" x 0.018" PHOSPHOR BRONZE, 50µINCH MIN OVERALL NICKEL UNDERPLATE, WITH SELECT 50µINCH MIN HARD GOLD FINISH PLATE
 - SOLDERTAILS WITH 100µINCH MIN MATTE TIN AND/OR SAC SOLDER DIP
- △ RJ45 JACK CAVITY CONFORMS TO FCC RULES AND REGULATIONS PART 68, SUB PART F.
- △ MAGNETICS
 - IMPEDANCE 100 OHMS
 - TURNS RATIO (CHP CABLE) TX = 1:1, RX = 1:1
 - OPEN CIRCUIT INDUCTANCE (OCL) 350µH MIN @100MHz, 0 IVRMS, 8mADC BIAS FROM 0°C TO 70°C, TX AND RX
 - POE CURRENT 350mADC MAX
 - PERFORMANCE @ 25°C
 - INSERTION LOSS (IL) 1.1dB MAX FROM 0.5MHz TO 100MHz
 - RETURN LOSS (RL) 18dB MIN FROM 0.5MHz TO 30MHz
 - 18-20LOG(F/30)dB MIN FROM 30.1MHz TO 60MHz
 - 12dB MIN FROM 60.1MHz TO 80MHz
 - CROSSTALK ATTENUATION: 35dB MIN FROM 0.5MHz TO 40MHz
 - 33-20LOG(F/50)dB MIN FROM 40.1MHz TO 100MHz
 - COMMON MODE REJECTION RATIO (CMRR) 30dB MIN FROM 0.5MHz TO 100MHz
 - ISOLATION VOLTAGE COMPLIES WITH IEEE802.3af 2003, PARA 33.4.1, ITEM a
- 4. OPERATING TEMPERATURE FROM 0° TO -70°C
- △ INDICATED CONNECTIONS ARE FOR NIC CONFIGURATION. THE MAGNETICS ARE SYMMETRIC, AND THEREFORE ARE AUTO-MID/MIX CAPABLE
- △ TYCO ELECTRONICS LOGO, PART NUMBER, DATE CODE, COUNTRY OF ORIGIN AND AGENCY APPROVAL MARKING IN APPROXIMATE LOCATION SHOWN
- 7. THE PART IS RECOMMENDED FOR WAVE SOLDERING PROCESS. PEAK WAVE SOLDERING TEMPERATURE IS 265°C, 10SECONDS MAX

6605836-2
PART NUMBER

THIS DRAWING IS A CONTROLLED DOCUMENT		UNCLASSIFIED		CLASSIFIED	
DATE	BY	DATE	BY	DATE	BY
08/03/2008	AA	08/03/2008	AA	08/03/2008	AA
CUSTOMER DRAWING		CUSTOMER DRAWING		CUSTOMER DRAWING	
1X1 MAG45(N), 7D15P1 SCHEMATIC, 715 SERIES CIRCUIT, POE APPLICATION, SHIELDED, WITH NO LEADS		1X1 MAG45(N), 7D15P1 SCHEMATIC, 715 SERIES CIRCUIT, POE APPLICATION, SHIELDED, WITH NO LEADS		1X1 MAG45(N), 7D15P1 SCHEMATIC, 715 SERIES CIRCUIT, POE APPLICATION, SHIELDED, WITH NO LEADS	
A1		A1		A1	
00779		00779		00779	
6605836		6605836		6605836	
NTS		NTS		NTS	
1		1		1	
R		R		R	