



IMPORTANT NOTICE

10 December 2015

1. Global joint venture starts operations as WeEn Semiconductors

Dear customer,

As from November 9th, 2015 NXP Semiconductors N.V. and Beijing JianGuang Asset Management Co. Ltd established Bipolar Power joint venture (JV), **WeEn Semiconductors**, which will be used in future Bipolar Power documents together with new contact details.

In this document where the previous NXP references remain, please use the new links as shown below.

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Thank you for your cooperation and understanding,

WeEn Semiconductors



DATA SHEET

BUJ100 Silicon Diffused Power Transistor

Product specification

September 1999



Silicon Diffused Power Transistor

BUJ100

GENERAL DESCRIPTION

High-voltage, high-speed planar-passivated npn power switching transistor in the TO92 envelope intended for use in compact fluorescent lamps and low power electronic lighting ballasts, converters and inverters, etc.

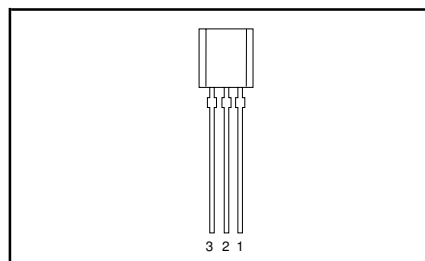
QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
V_{CESM}	Collector-emitter voltage peak value	$V_{BE} = 0\text{ V}$	-	700	V
V_{CBO}	Collector-Base voltage (open emitter)		-	700	V
V_{CEO}	Collector-emitter voltage (open base)		-	400	V
I_C	Collector current (DC)		-	1.0	A
I_{CM}	Collector current peak value		-	2.0	A
P_{tot}	Total power dissipation	$T_{lead} \leq 25\text{ }^\circ\text{C}$	-	2	W
V_{CEsat}	Collector-emitter saturation voltage	$I_C = 0.75\text{ A}; I_B = 150\text{ mA}$	0.24	1.0	V
h_{FE}		$I_C = 0.75\text{ A}; V_{CE} = 5\text{ V}$	14	20	
t_{fi}	Fall time (Inductive)	$I_C = 1.0\text{ A}; I_{BON} = 200\text{ mA}$	50	70	ns

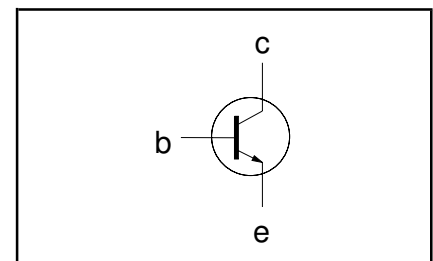
PINNING - TO92

PIN	DESCRIPTION
1	Emitter
2	Collector
3	Base

PIN CONFIGURATION



SYMBOL



LIMITING VALUES

Limiting values in accordance with the Absolute Maximum Rating System (IEC 134)

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_{CESM}	Collector to emitter voltage	$V_{BE} = 0\text{ V}$	-	700	V
V_{CEO}	Collector to emitter voltage (open base)		-	400	V
V_{CBO}	Collector to base voltage (open emitter)		-	700	V
I_C	Collector current (DC)		-	1.0	A
I_{CM}	Collector current peak value		-	2.0	A
I_B	Base current (DC)		-	0.5	A
I_{BM}	Base current peak value		-	1.0	A
P_{tot}	Total power dissipation	$T_{lead} \leq 25\text{ }^\circ\text{C}$	-	2	W
T_{stg}	Storage temperature		-65	150	$^\circ\text{C}$
T_j	Junction temperature		-	150	$^\circ\text{C}$

THERMAL RESISTANCES

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
$R_{th\ j-lead}$	Thermal resistance junction to lead		-	60	K/W
$R_{th\ j-a}$	Thermal resistance junction to ambient	pcb mounted; lead length = 4mm	150	-	K/W

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STATIC CHARACTERISTICS

T_{lead} = 25 °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I _{CES} , I _{CBO} I _{CES}	Collector cut-off current ¹	V _{BE} = 0 V; V _{CE} = V _{CESMmax} V _{BE} = 0 V; V _{CE} = V _{CESMmax} T _j = 125 °C	-	0.8 2.0	100 500	μA μA
I _{CEO} I _{EBO} V _{CEOsust}	Collector cut-off current Emitter cut-off current Collector-emitter sustaining voltage	V _{CEO} = V _{CEOMmax} (400V) V _{EB} = 9 V; I _C = 0 A I _B = 0 A; I _C = 10mA; L = 25 mH	-	- 0.05	100 100	μA μA
V _{CEsat} V _{BEsat}	Collector-emitter saturation voltage Base-emitter saturation voltage	I _C = 0.75 A; I _B = 0.15 A I _C = 0.75 A; I _B = 0.15 A	400	-	-	V
h _{FE} h _{FE} h _{FE}	DC current gain	I _C = 10mA; V _{CE} = 5 V I _C = 100mA; V _{CE} = 5 V I _C = 0.75 A; V _{CE} = 5 V	-	0.24 0.93	1.0 1.3	V V
			11 12.5 9	20 21 14	27 31 20	

DYNAMIC CHARACTERISTICS

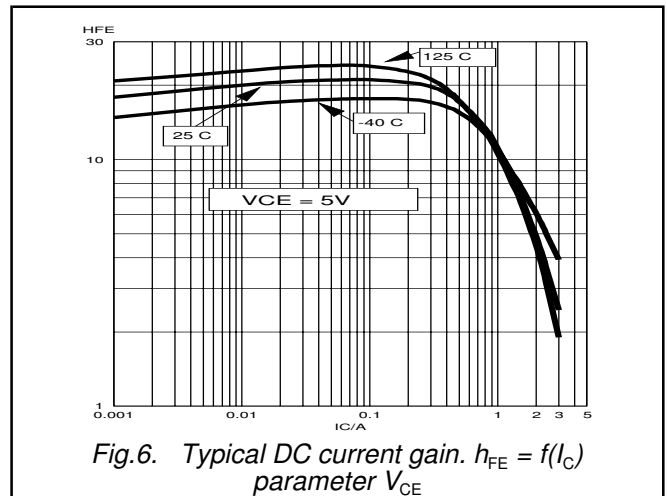
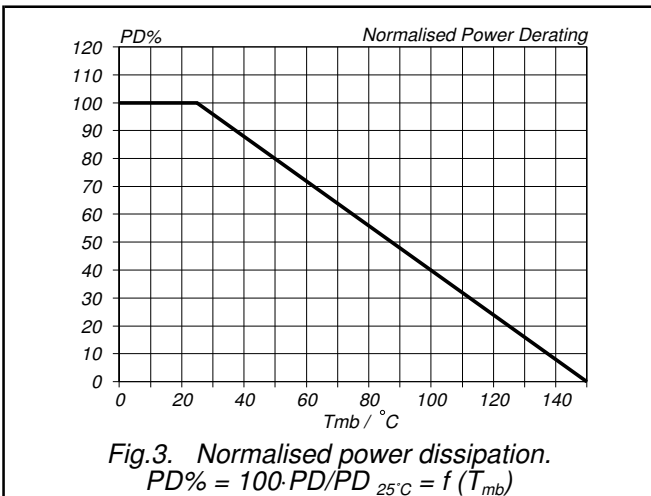
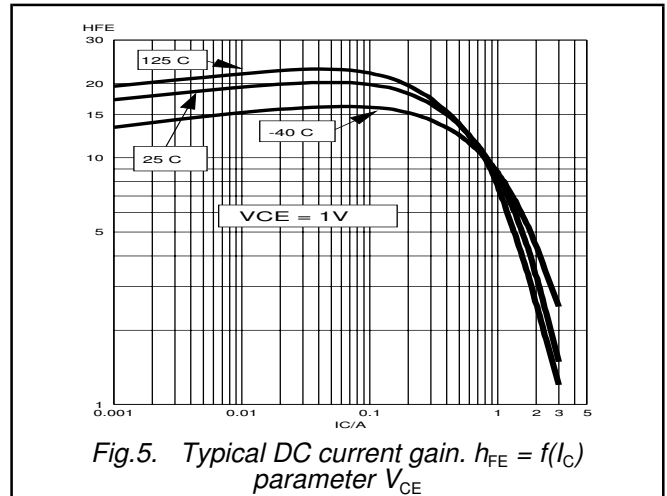
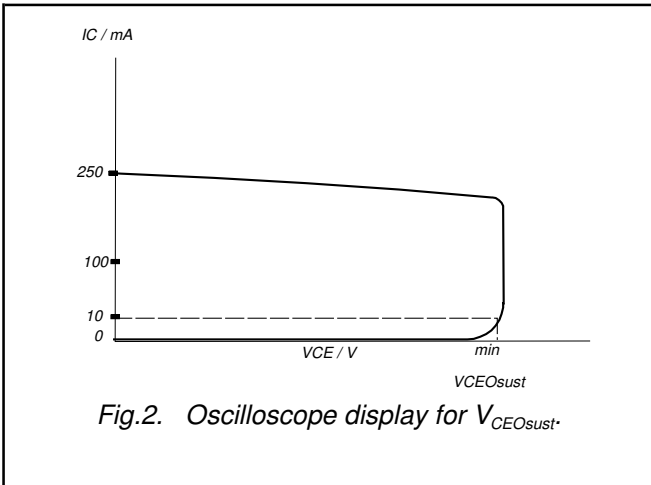
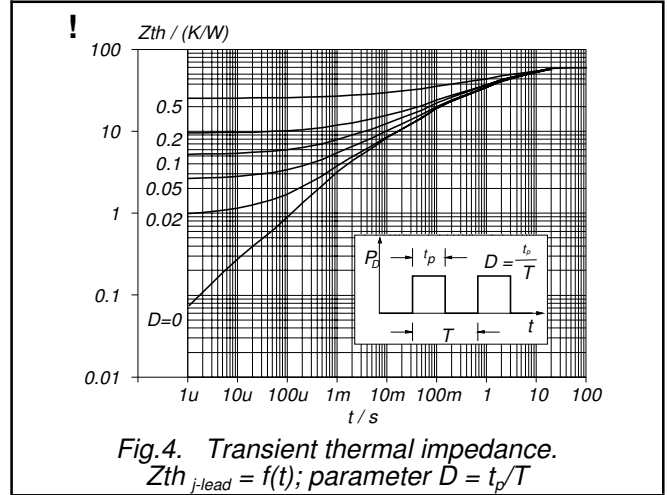
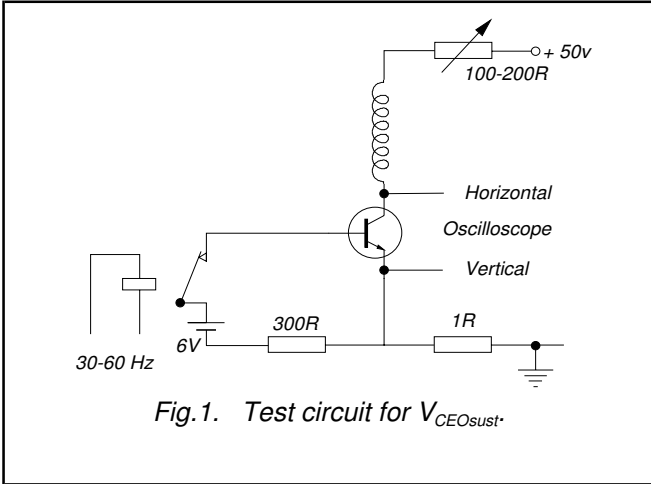
T_{lead} = 25 °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
	Switching times (resistive load)	I _{Con} = 1.0 A; I _{Bon} = -I _{Boff} = 200mA; R _L = 75 ohms; V _{BB2} = 4 V;			
t _{on} t _s t _f	Turn-on time Turn-off storage time Turn-off fall time		0.65 0.88 250	0.88 1.2 338	μs μs ns
	Switching times (inductive load)	I _{Con} = 1.0 A; I _{Bon} = 200mA; L _B = 1 μH; -V _{BB} = 5 V			
t _s t _f	Turn-off storage time Turn-off fall time		0.51 50	0.7 70	μs ns
	Switching times (inductive load)	I _{Con} = 1.0 A; I _{Bon} = 200mA; L _B = 1 μH; -V _{BB} = 5 V; T _j = 100 °C			
t _s t _f	Turn-off storage time Turn-off fall time		- -	1.4 130	μs ns

¹ Measured with half sine-wave voltage (curve tracer).

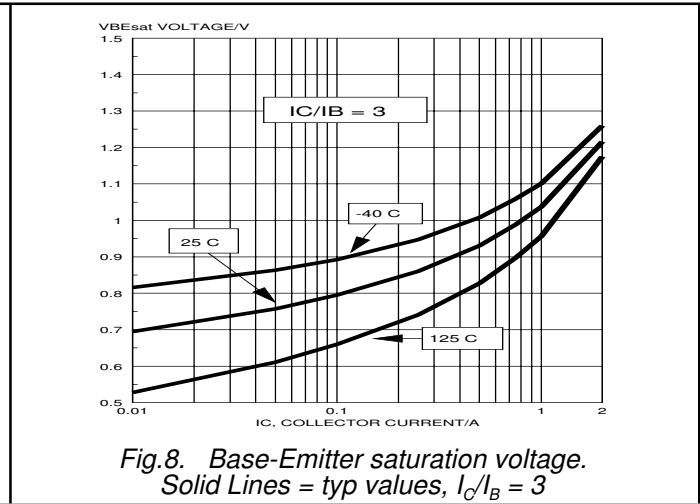
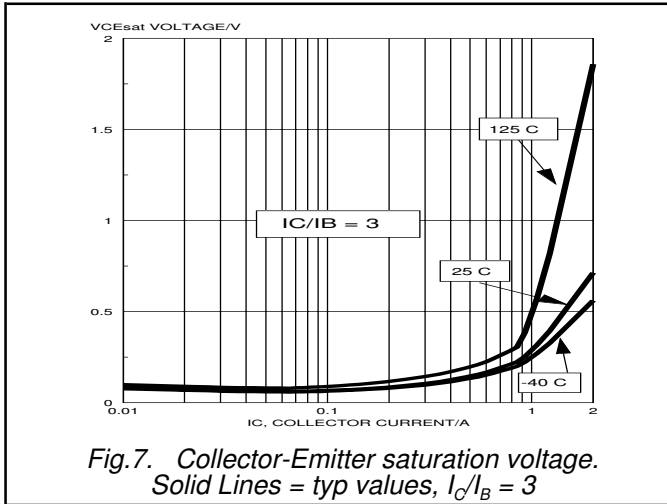
Silicon Diffused Power Transistor

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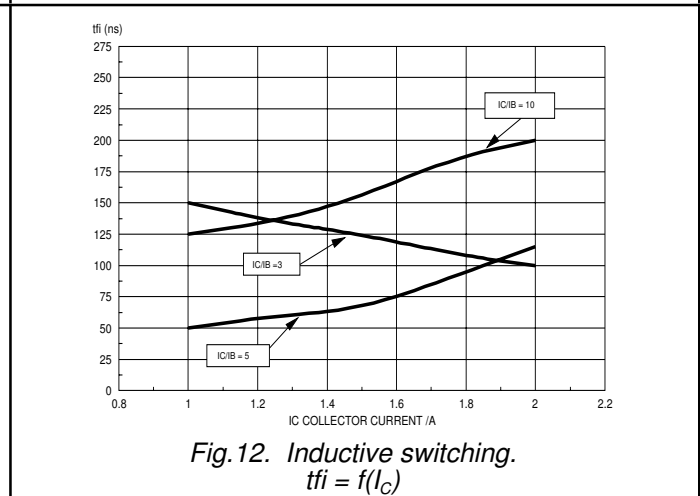
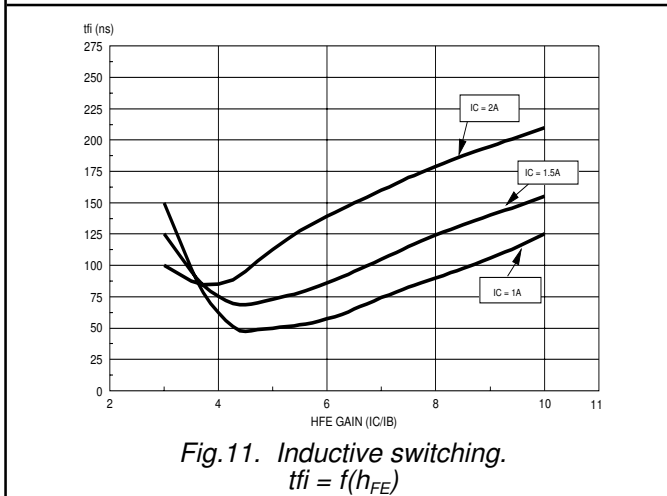
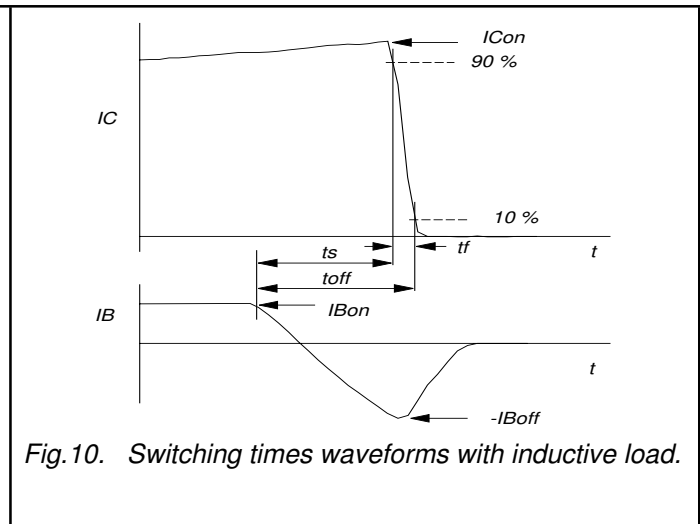
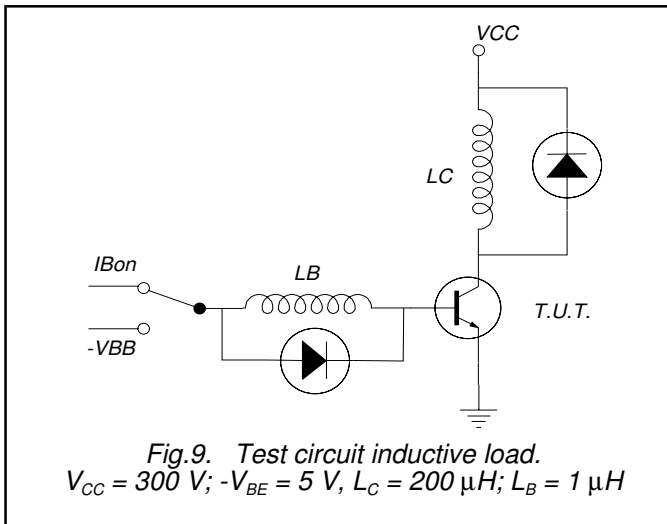


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INDUCTIVE SWITCHING



Silicon Diffused Power Transistor

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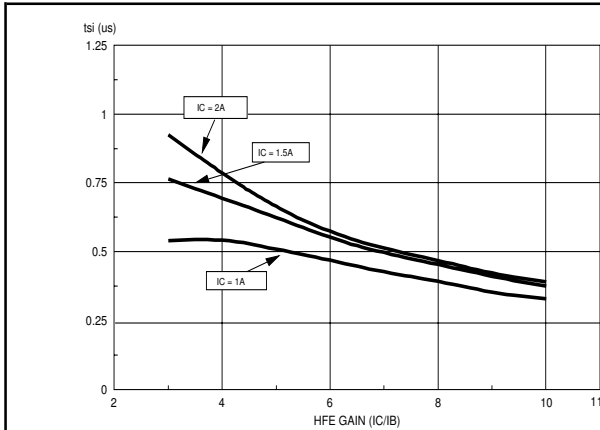


Fig.13. Inductive switching.
 $t_{si} = f(h_{FE})$

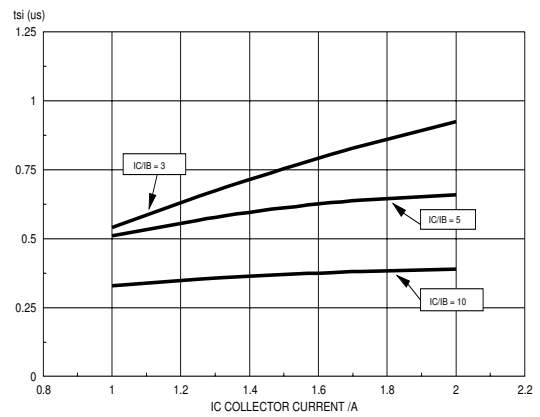


Fig.14. Inductive switching.
 $t_{si} = f(I_C)$

RESISTIVE SWITCHING

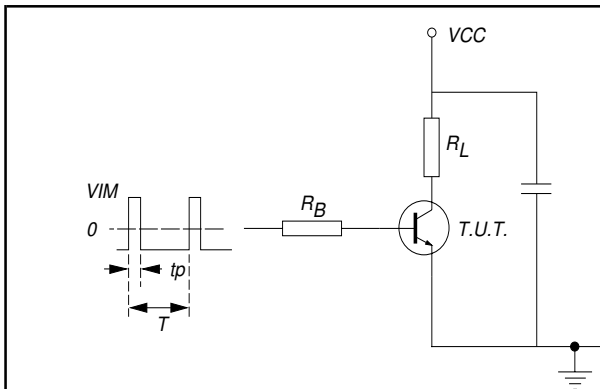


Fig.15. Test circuit resistive load. $V_{IM} = -6$ to $+8$ V
 $V_{CC} = 250$ V; $t_p = 20$ μ s; $\delta = t_p / T = 0.01$.
 R_B and R_L calculated from I_{Con} and I_{Bon} requirements.

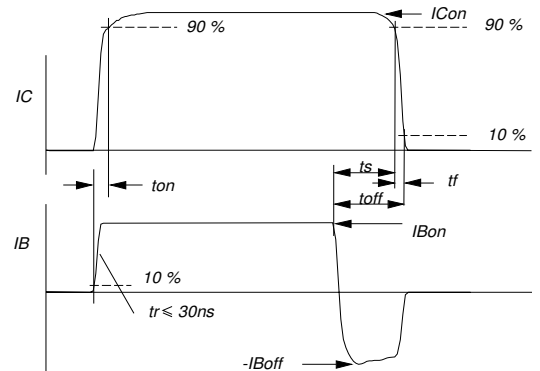


Fig.16. Switching times waveforms with resistive load.

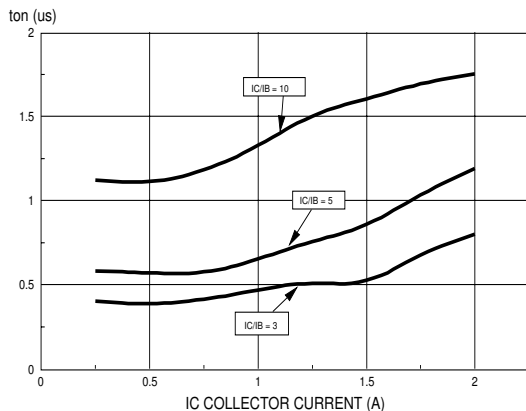


Fig.17. Resistive switching.
 $t_{on} = f(I_C)$

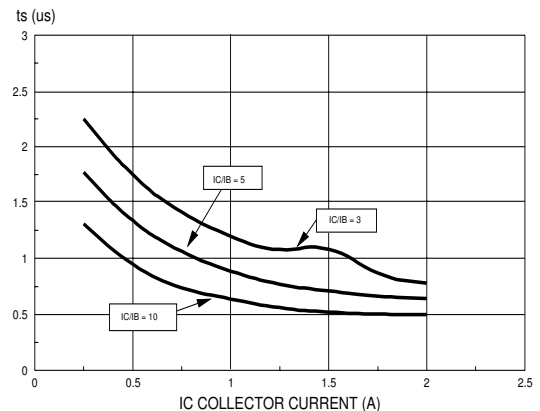
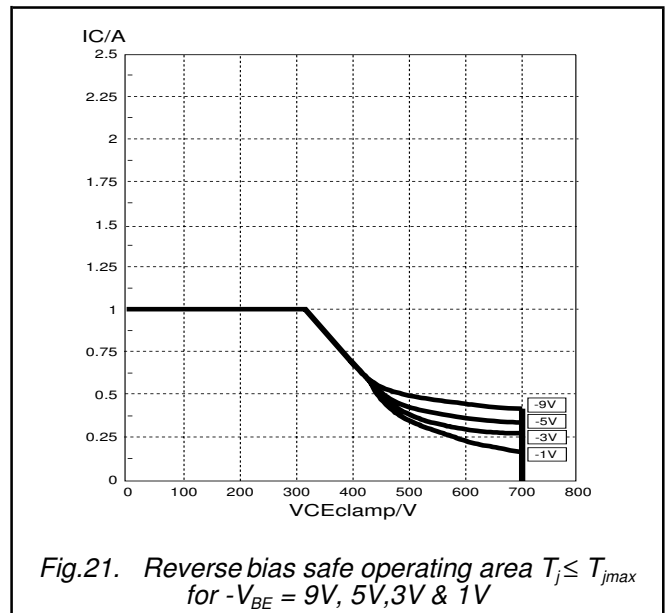
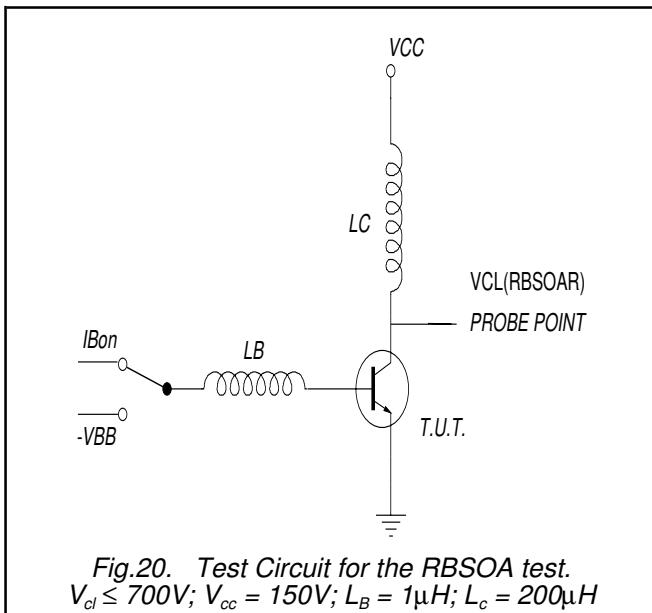
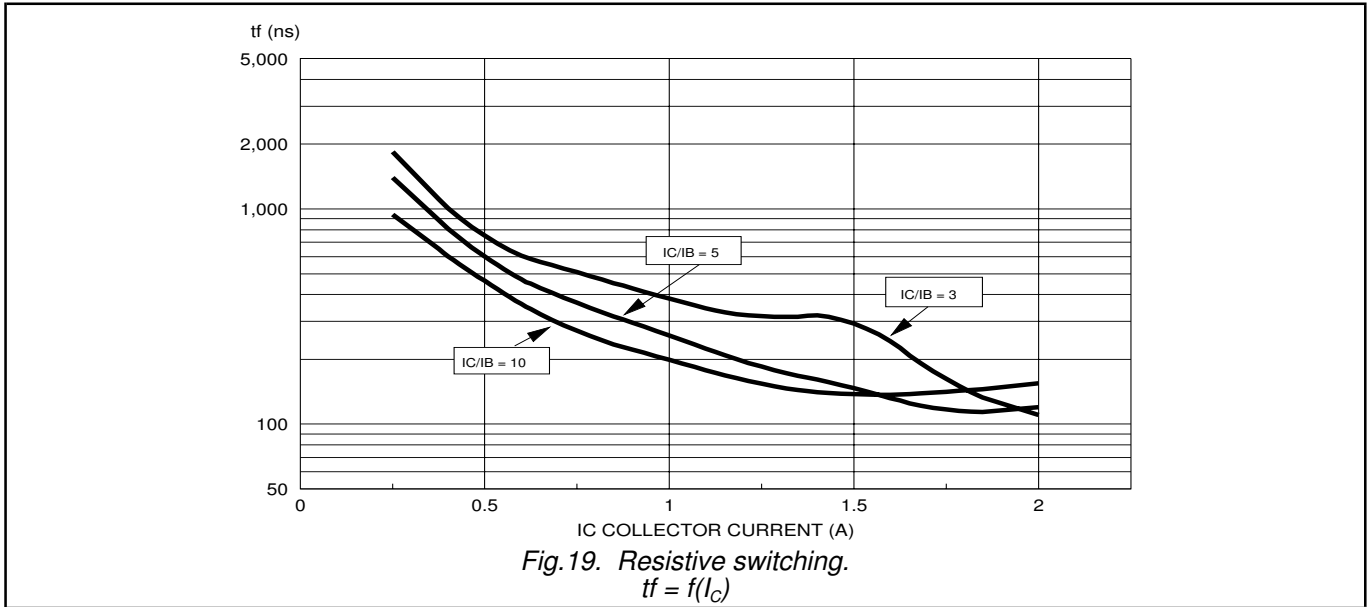


Fig.18. Resistive switching.
 $t_s = f(I_C)$

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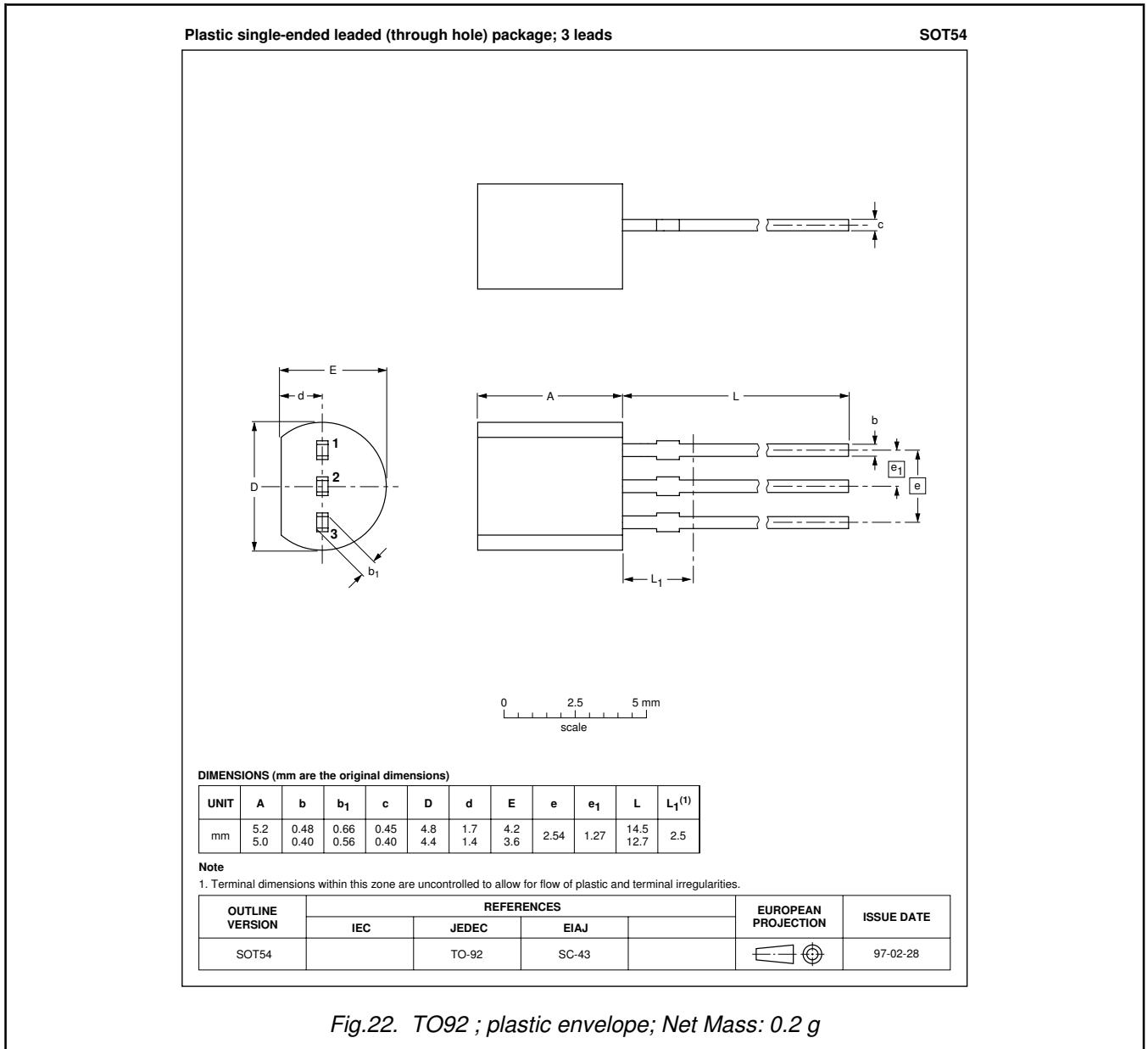
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MECHANICAL DATA



Notes

1. Epoxy meets UL94 V0 at 1/8".

Legal information

DATA SHEET STATUS

DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

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Contact information

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